

10/709,167

Don
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IN THE SPECIFICATION

Please amend the following paragraph [0030]³² as follows.

[0031] In file management, FAT1, FAT2 and FDB addresses are altered constantly when the host processes data writing operations. In such a case, if the judging method by using ~~of a~~ plurality sets of mother and child blocks is applied therein, not only the efficiency can be upgraded but also the erase frequency of the flash memory can be reduced. Therefore, the above judging method ~~when~~ by applying ~~applied to a~~ plurality sets of mother and child blocks can substantially aid in increasing the data storing speed as well as reduce the erasing frequency while the flash memory altering the fixed address. Referring to Fig. 19, a flow chart of a writing procedure according to an embodiment of the present invention, when the manager receives writing command from the host, the procedure for the flash memory is executed is as follows:

in step (701), the writing command is received;

~~In~~ in step (702), checking whether ~~the~~ a block for writing has a set of mother and child blocks, if yes, the procedure proceeds to step (706) ~~for yes, if no, the procedure proceeds to step~~ or (703) for no;

in step (703), ~~the flash memory is checked~~ checking whether it the flash memory having ~~has~~ a set of mother and child blocks, if yes, the procedure proceeds to step (704), if not, the procedure proceeds to step (705);

in step (704), whether the numbers of the used set of mother and child blocks reach the preset number in the manager is judged, if yes, a set of the mother and child blocks is located and combined as a new block, then this block defined as ~~the~~ a mother block ~~to~~ and ~~proceeds~~ the step (7065), if not, the procedure proceeds to step (705) directly;